

WEST Search History

DATE: Friday, April 16, 2004

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DB=USPT; PLUR=YES; OP=ADJ

<input type="checkbox"/>	L3	6477200.pn. and regulat\$	1
<input type="checkbox"/>	L2	5737614.pn. and regulat\$	1
<input type="checkbox"/>	L1	power near3 regulat\$ near3 (ic or integrated circuit) near5 dynamic\$	5

END OF SEARCH HISTORY

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L1: Entry 1 of 5

File: USPT

Nov 5, 2002

DOCUMENT-IDENTIFIER: US 6477200 B1

TITLE: Multi-pair gigabit ethernet transceiver

Detailed Description Text (116):

In the context of an exemplary integrated circuit-type bidirectional communication system, a further aspect of the invention might be characterized as a system and method for adaptively and dynamically regulating the power consumption of an integrated circuit communication system as a function of particular, user defined signal quality metrics. Signal quality metrics might include a signal's bit error rate (BER), a signal-to-noise ratio (SNR) specification, noise margin figure, dynamic range, or the like. Indeed, signal quality is a generalized term used to describe a signal's functional fidelity.

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L1: Entry 3 of 5

File: USPT

Oct 1, 2002

DOCUMENT-IDENTIFIER: US 6459746 B2

TITLE: Multi-pair gigabit ethernet transceiver

Detailed Description Text (116):

In the context of an exemplary integrated circuit-type bidirectional communication system, a further aspect of the invention might be characterized as a system and method for adaptively and dynamically regulating the power consumption of an integrated circuit communication system as a function of particular, user defined signal quality metrics. Signal quality metrics might include a signal's bit error rate (BER), a signal-to-noise ratio (SNR) specification, noise margin figure, dynamic range, or the like. Indeed, signal quality is a generalized term used to describe a signal's functional fidelity.

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L1: Entry 5 of 5

File: USPT

Apr 7, 1998

DOCUMENT-IDENTIFIER: US 5737614 A

TITLE: Dynamic control of power consumption in self-timed circuits

Detailed Description Text (18):

Thus, it can be seen how the present invention allows the dynamic regulation of power consumption in an integrated circuit by controlling the data transfer rate through various stages in a pipelined self-timed circuit in accordance with a sensed characteristic such as heat or electric current flow.